

FIG. 1

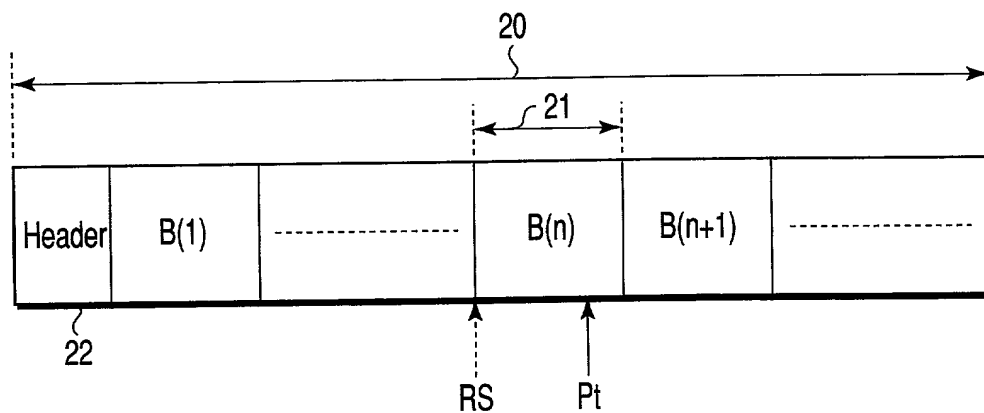


FIG. 2

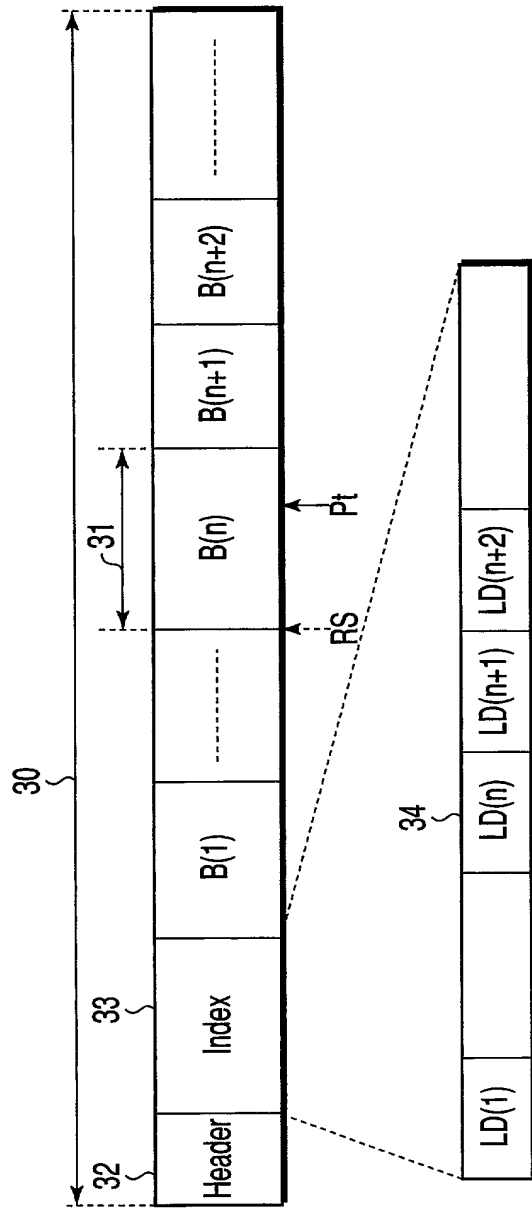


FIG. 3

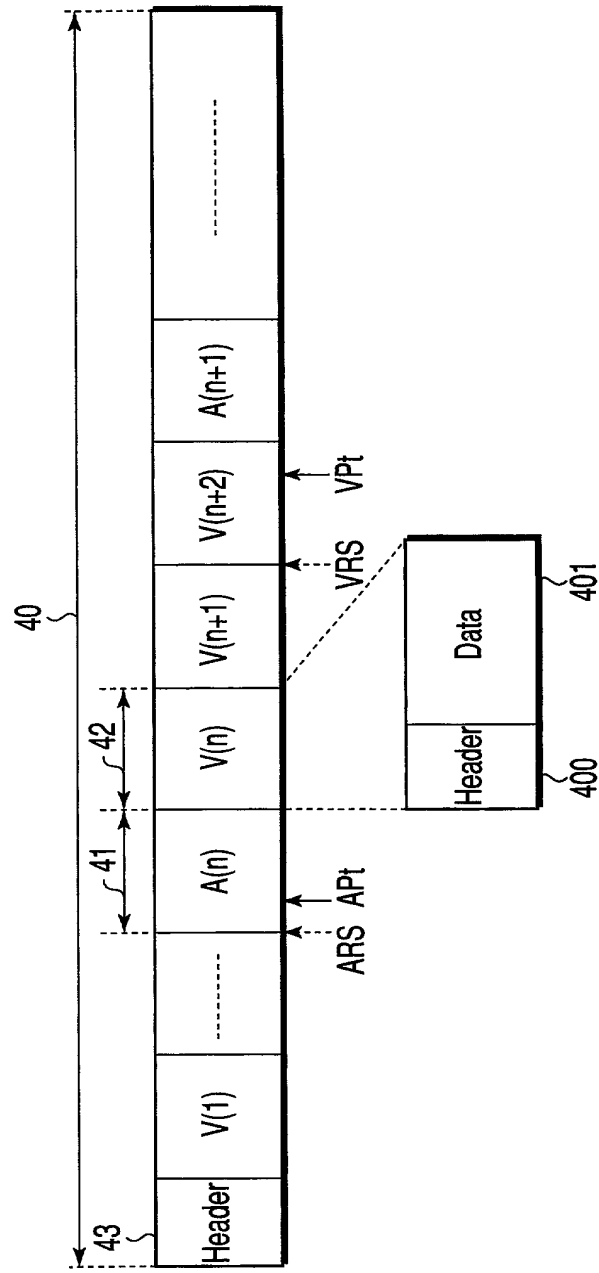


FIG. 4

FIG. 5A

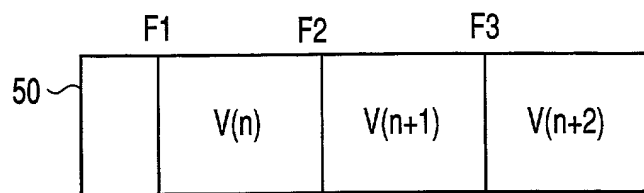


FIG. 5B

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Block	Buffer address	Time
n	F1	Tb
n+1	F2	Tc
n+2	F3	Td
⋮	⋮	⋮

FIG. 6A

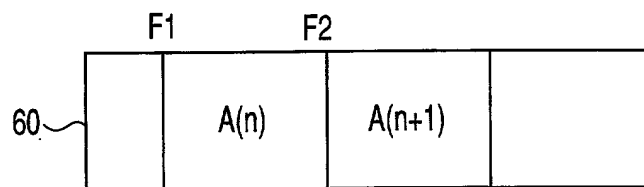
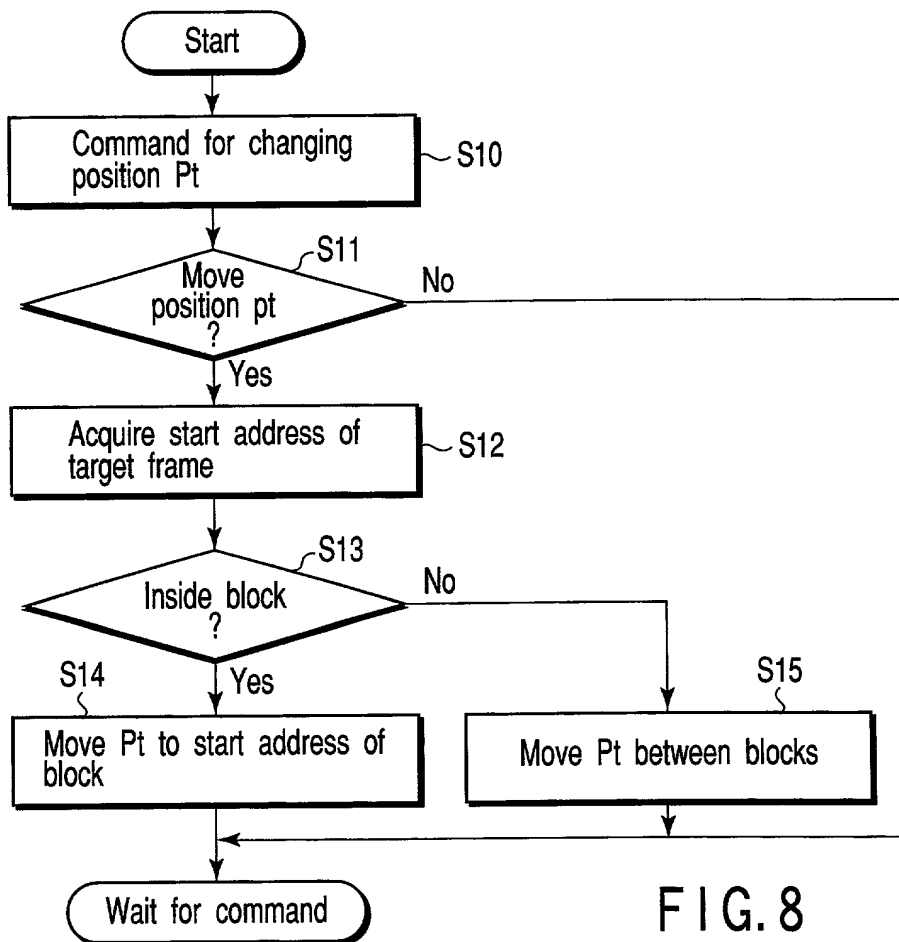
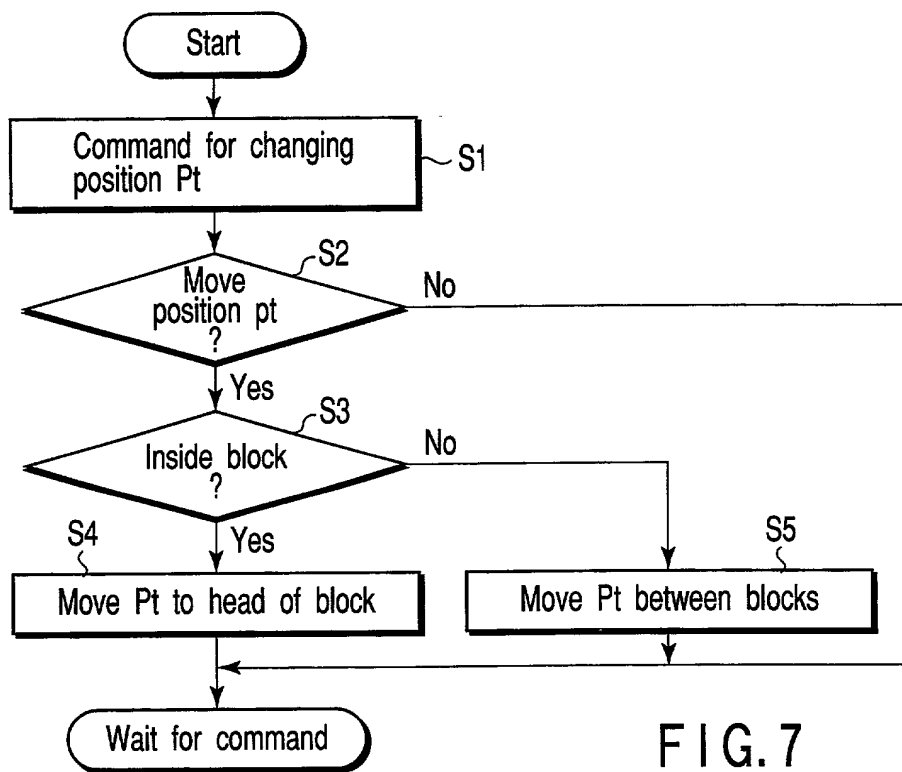


FIG. 6B

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Block	Buffer address	Time
n	F1	Ta
n+1	F2	Te
⋮	⋮	⋮



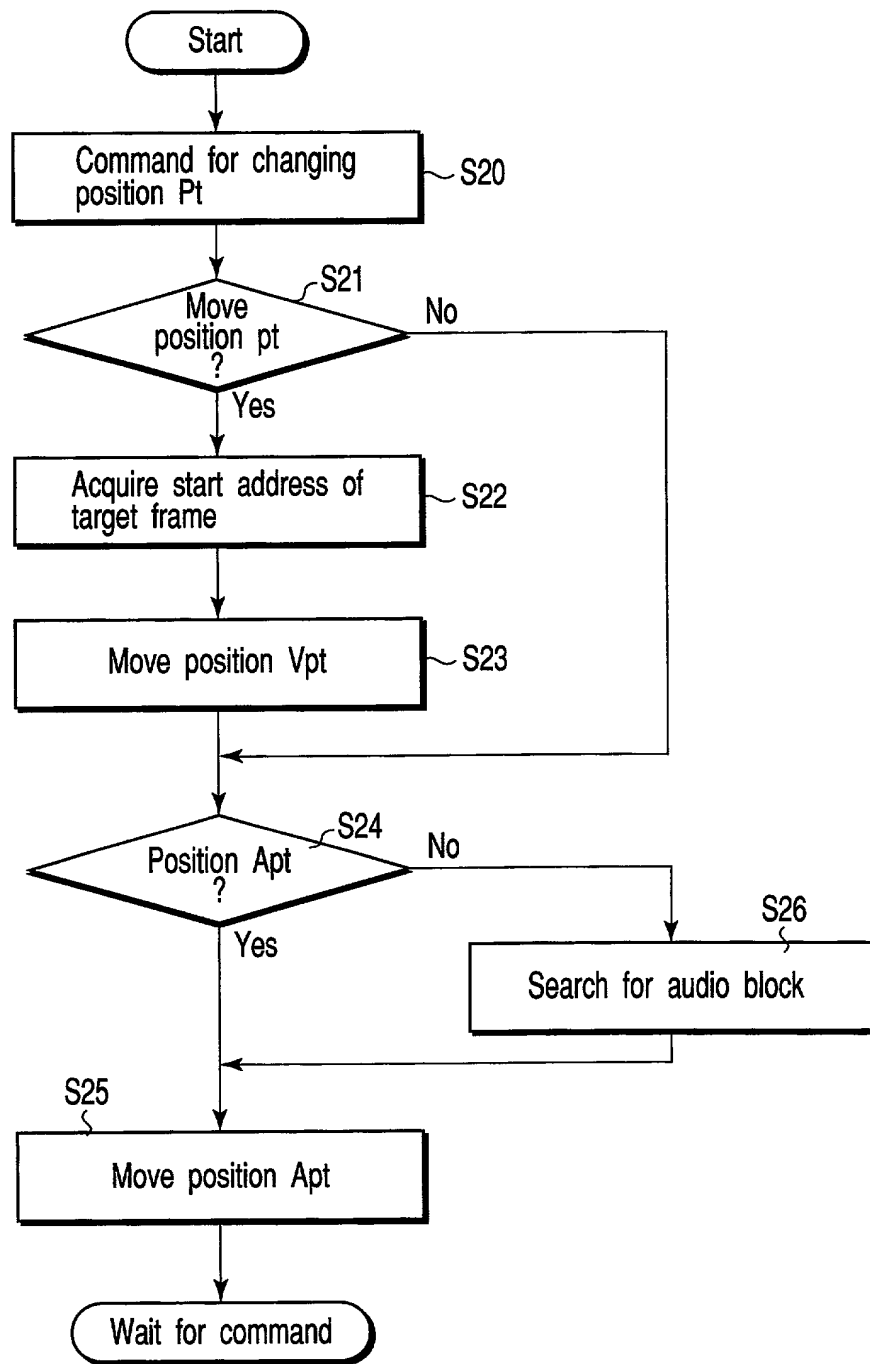


FIG. 9